

FIGURE 1

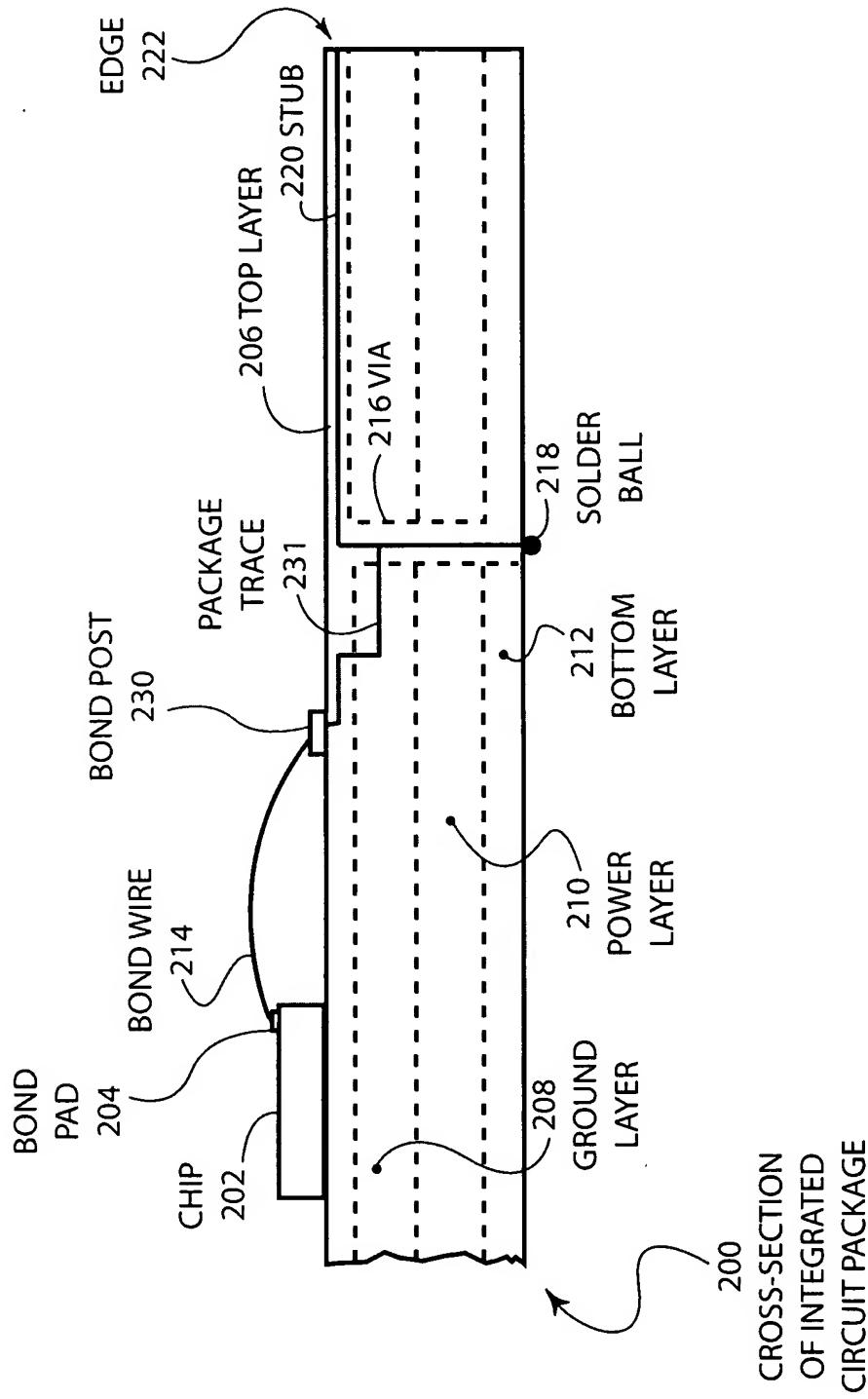


FIGURE 2

CROSS-SECTION
OF INTEGRATED
CIRCUIT PACKAGE

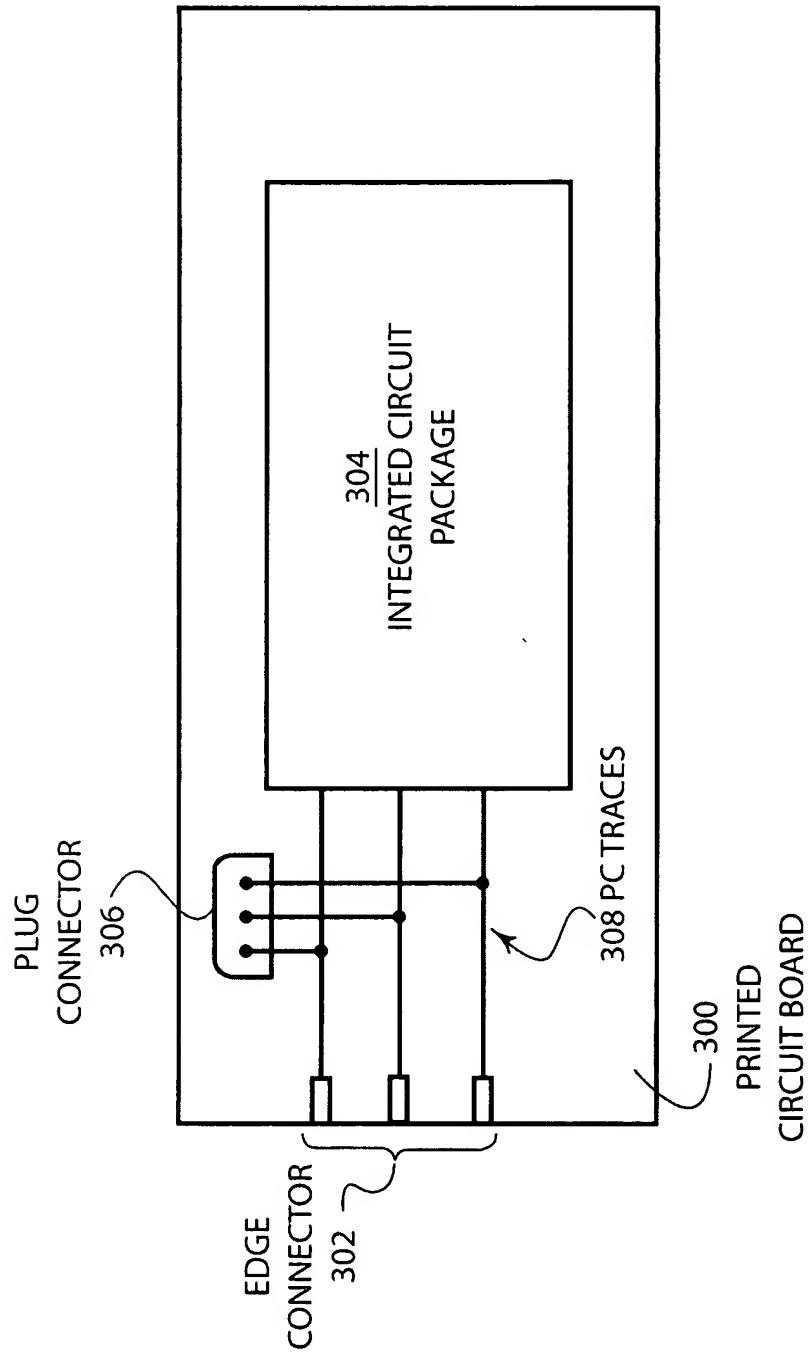


FIGURE 3

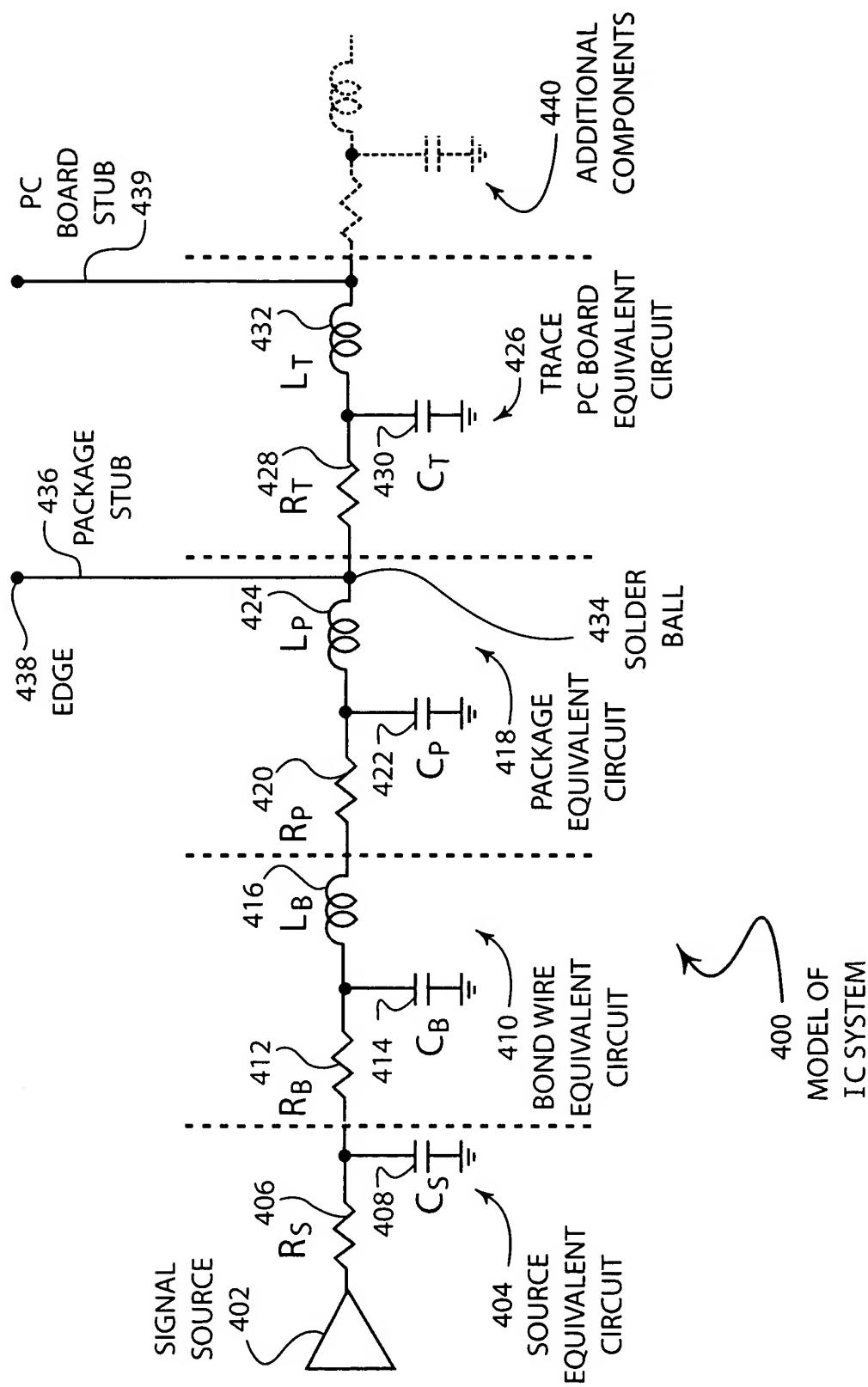


FIGURE 4

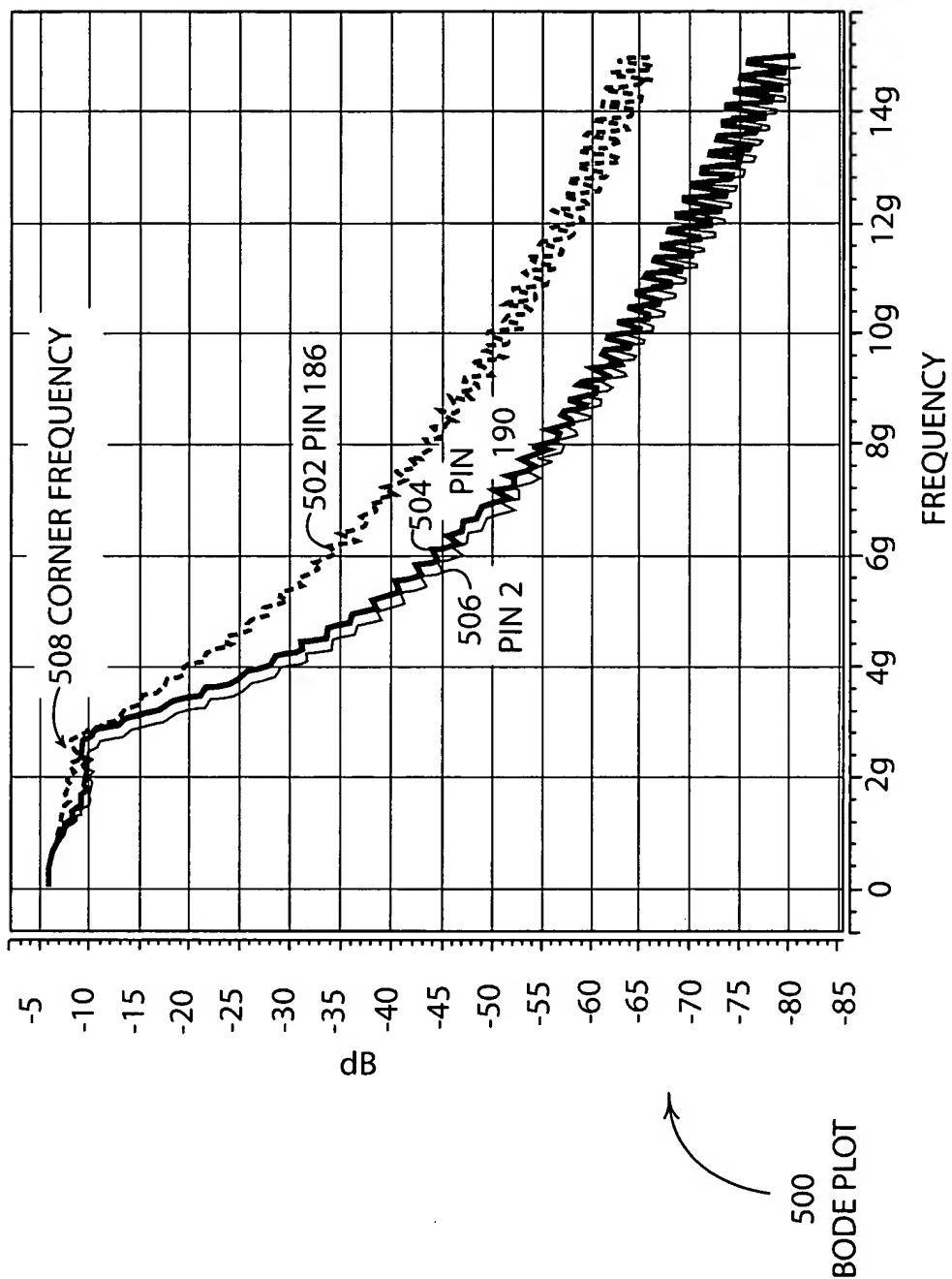


FIGURE 5

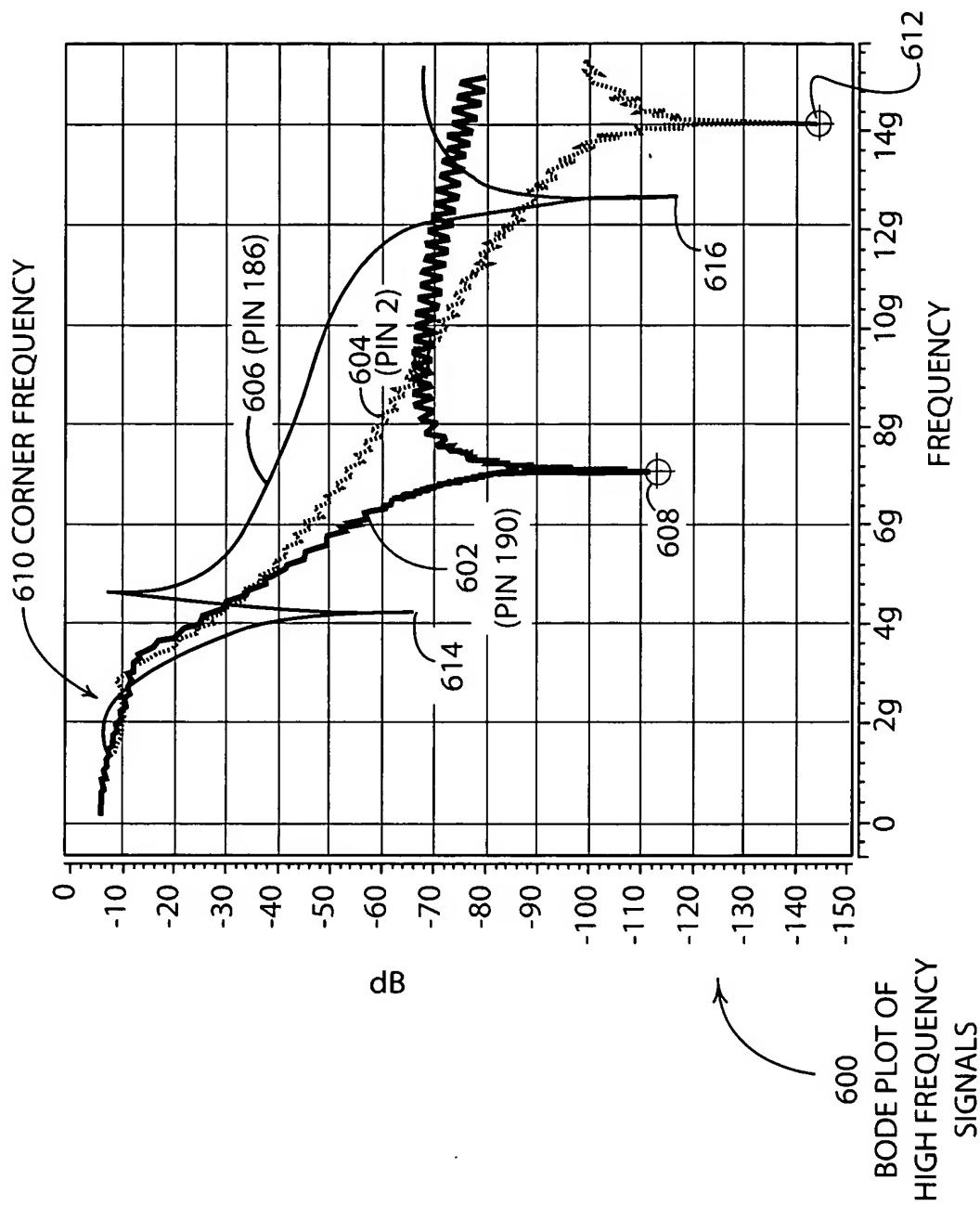


FIGURE 6

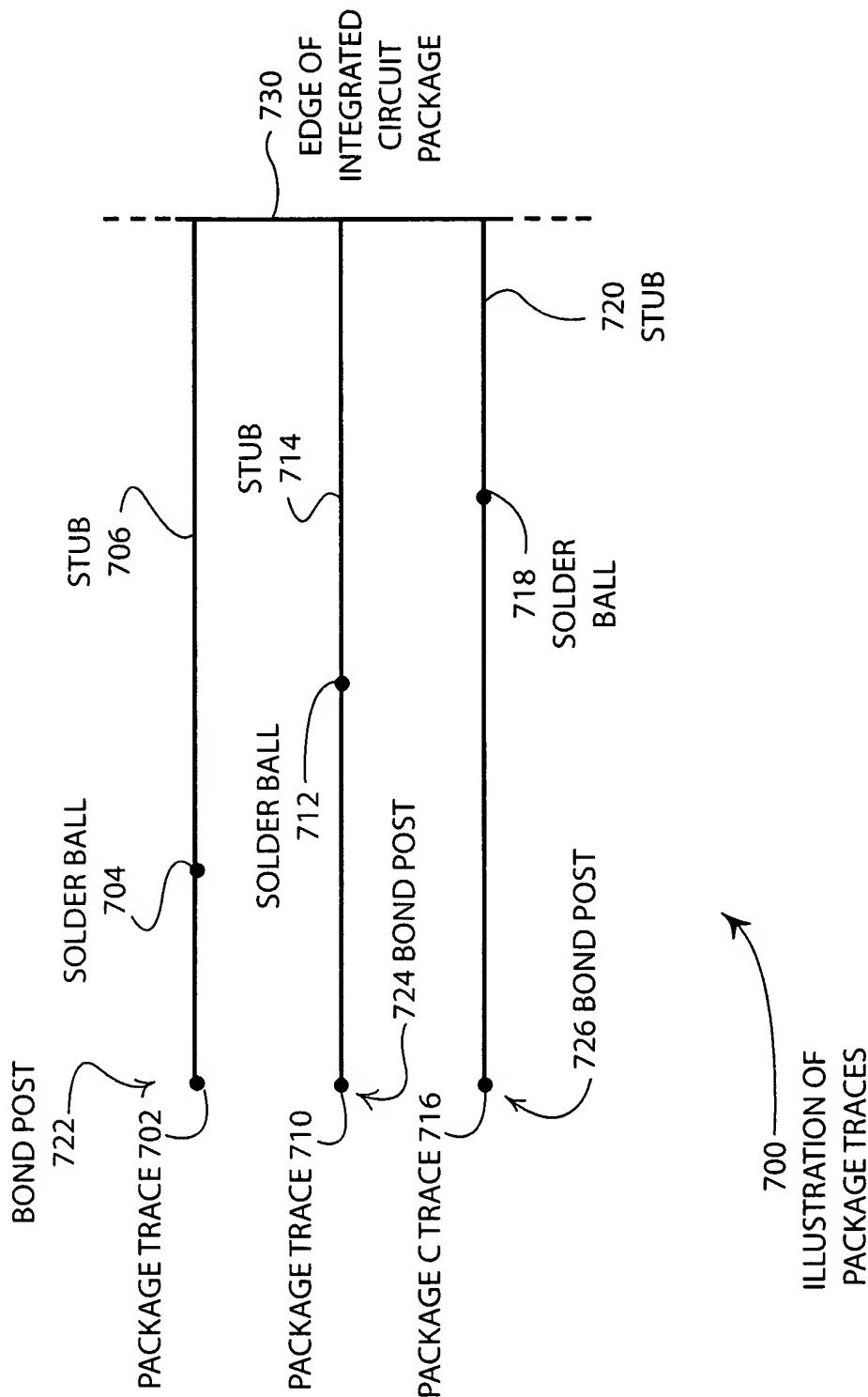


FIGURE 7